

Verificación de la implementación mediante su emulación en un FPGA.												
Implementación en HDL y su emulación en un FPGA												
Escritura de Tesis												

Materias por cursar

1. Tópicos Selectos de Ingeniería Electrónica
2. Codificación de Datos

Bibliografía

1. Nikolova, M.; Steidl, G., "Fast Hue and Range Preserving Histogram Specification: Theory and New Algorithms for Color Image Enhancement," Image Processing, IEEE Transactions on , vol.23, no.9, pp.4087,4100, Sept. 2014
2. Shih-Chia Huang; Wen-Chieh Chen, "A New Hardware-Efficient Algorithm and Reconfigurable Architecture for Image Contrast Enhancement," Image Processing, IEEE Transactions on , vol.23, no.10, pp.4426,4437, Oct. 2014
3. Agarwal, T.K.; Tiwari, M.; Lamba, S.S., "Modified Histogram based contrast enhancement using Homomorphic Filtering for medical images," Advance Computing Conference (IACC), 2014 IEEE International , vol., no., pp.964,968, 21-22 Feb. 2014
4. Celik, T., "Spatial Entropy-Based Global and Local Image Contrast Enhancement," Image Processing, IEEE Transactions on , vol.23, no.12, pp.5298,5308, Dec. 2014